

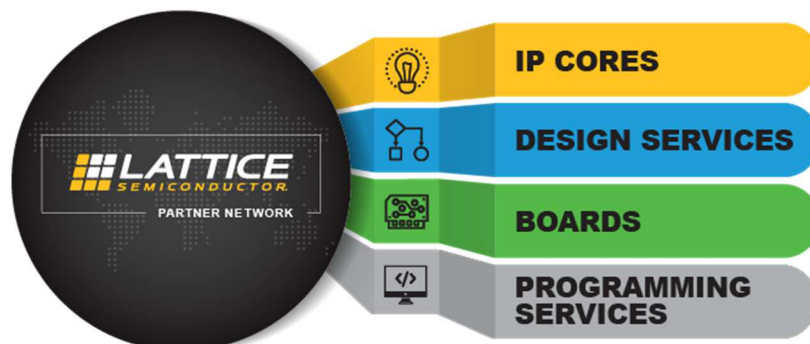
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Shikino Joins Lattice Partner Network

Providing FPGA Design Services & IP Cores Focused on Image Processing and High-Speed Interfaces

Shikino High-Tech Co., Ltd. (hereafter "Shikino ") has joined Lattice Semiconductor's Partner Network (hereafter "Lattice") as a "Design Services Partner and IP Provider Partner."

Shikino boasts extensive FPGA design experience, particularly in image processing and high-speed interfaces, and offers a lineup of IP cores centered around JPEG and ISP. By becoming a partner of Lattice, we are now able to seamlessly provide our FPGA Design Services and IP cores to users utilizing Lattice FPGAs.



Lattice Partner Network is a growing FPGA ecosystem with trusted technology partners offering Lattice-based solutions from IP cores, design services, boards, and programming services to help customers innovate and accelerate their design goals. Visit Lattice Partner Network webpage for more information.

Partner Network webpage: <https://www.latticesemi.com/Support/Partners>

**Depending on the needs of our users, we can also offer solutions that combine FPGA Design Services and IP cores.*

【Contact】

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